



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **WATANABE, Kenichi**

Group Art Unit: **2815**

Serial No.: **09/987,012**

Examiner: **Chris C. Chu**

Filed: **November 13, 2001**

P.T.O. Confirmation No.: 6363

For: **SEMICONDUCTOR WAFER DEVICE HAVING SEPARATED CONDUCTIVE
PATTERNS IN PERIPHERAL AREA AND ITS MANUFACTURING METHOD**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 25, 2005

Sir:

Prior to further examination on the merits, please amend the above-identified application
as follows: